



- Made of cross linked polyolefin , inner coated with adhesive at the ends
- Provide sealing protection over multi-core cable crutch , including 2-, 3-, 4-, 5-core breakouts
- Meeting with varieties of configuration requirements
- Continuous operation temperature: -45 °C to 105 °C
- Shrink temperature: start at 110 °C, and fully recovered at 135 °C

Technical Data

Property	Test Method	Standard Value		
		Insulated Breakout	Oil Barrier Breakout	Conductive Breakout
Operation Temperature	IEC 216	-45°C to 105°C	-45°C to 105°C	-45°C to 105°C
Tensile Strength	ASTM-D-2671	≥12MPa	≥11MPa	≥12MPa
Elongation at Break (120°C,168 hrs)	ASTM-D-2671	≥300%	≥300%	≥300%
Tensile Strength after Aging (120°C,168 hrs)	ASTM-D-2671	≥10MPa	≥9MPa	≥10MPa
Elongation at Break after Aging	ASTM-D-2671	≥230%	≥230%	≥230%
Tensile Strength after Dipping	ASTM-D-2671	—	≥10MPa	—
Elongation at Break after Dipping	ASTM-D-2671	—	≥230%	—
Dielectric Strength	IEC 60243	≥20kV/mm	≥20kV/mm	—

Water Absorption	ISO 62	$\leq 0.5\%$	$\leq 0.5\%$	$\leq 0.5\%$
Volume Resistivity	IEC 60093	$\geq 10^{14} \Omega \cdot \text{cm}$	$\geq 10^{14} \Omega \cdot \text{cm}$	$10^2 \sim 10^4 \Omega \cdot \text{cm}$

Selection Table

Product No.		Base Diameter		Finger Diameter
		As Supplied (Min)/mm	After Recovered (Max)/mm	As Supplied (Min)/mm
2 cores	WRSZT2-24/12 (0#)	24	12	12
	WRSZT2-38/16 (1#)	38	16	20
	WRSZT2-48/18 (2#)	48	18	25
	WRSZT2-60/24 (3#)	60	24	35
	WRSZT2-72/25 (4#)	72	25	40
3 cores	WRSZT3-24/16 (-2#)	24	16	11
	WRSZT3-48/22 (-1#)	48	22	18
	WRSZT3-60/28 (0#)	60	28	25
	WRSZT3-70/36 (1#) ☆	70	36	30
	WRSZT3-85/45 (2#) ☆	85	45	40
	WRSZT3-110/53 (3#) ☆	110	53	42
	WRSZT3-125/63 (4#) ☆	125	63	58
4 cores	WRSZT4-38/18 (0#)	38	18	10
	WRSZT4-50/24 (1#) ☆	50	24	16
	WRSZT4-70/32 (2#) ☆	70	32	23
	WRSZT4-80/44 (3#) ☆	80	44	30
	WRSZT4-90/44 (4#) ☆	90	44	35
	WRSZT4-110/53 (5#) WRSZT4-110/51 (5# lengthen)	110 110	53 51	40 40

5 cores	WRSZT5-42/21 (0#)	42	21	12
	WRSZT5-57/29 (1#)	57	29	16
	WRSZT5-70/38 (2#)	70	38	23
	WRSZT5-90/50 (3#)	90	50	30
	WRSZT5-120/47 (4#)	120	47	39

Remark: * means conductive breakout general standing for three cores breakout
☆ means PILC breakout general standing for three cores or four cores breakout

